

ABSTRACT OF THE DISCLOSURE

An image processing device is disclosed that is capable of increasing processing speed with a simple configuration and without increasing required memory capacity when editing a compressed image.

The image processing device includes an image dividing unit, an encoding unit, and an editing unit. The image dividing unit divides an input image into image sections. The encoding unit encodes individual image sections independently, and generates a code stream; the code stream includes code sections corresponding to the image sections. The editing unit edits an individual code section, for example, deletes or extracts an image section, or replaces an image section with another image, and outputs the resulting code stream.